

# ITW

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Masahiko Kawase

Serial No.:

10/629,092

Filed:

July 28, 2003

For:

METHOD OF PRODUCING

CHIP THERMISTOR

Group Art Unit:

3729

Examiner:

Unknown

Attorney Docket:

MURT P095X1

**CERTIFICATE OF MAILING** 

Date of Deposit: August 9, 2004

I hereby certify that this correspondence is, on the date shown above, being:

deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, Virginia 22313.

Typed or printed name of person mailing:

Deborah Neill

Signature of person mailing

# <u>INFORMATION DISCLOSURE STATEMENT</u> <u>PURSUANT TO 37 C.F.R. §1.56 AND §§1.97-1.98</u>

Commissioner for Patents Alexandria, Virginia 22313

Sir:

The following Form 1449 and copies of documents listed thereon are being filed herewith as a Disclosure Statement. Consideration of each of these documents by the Patent Examiner, and the making of each of them of record in the file of this application, are respectfully requested.

Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign

application not more than three months prior to the filing of this statement. A copy of this communication is also being submitted simultaneously herewith.

Documents J and K were each cited for disclosing an electronic component having a thermistor and a resistor compounded together. Documents L and M were each cited for disclosing a resistor formed inside a layered structure.

Respectfully submitted,

Keiichi Nishimura

Registration No. 29,093

August 9, 2004 BEYER WEAVER & THOMAS, LLP P.O. Box 778 Berkeley, CA 94704-0778

Telephone: (510) 843-6200 Telefax: (510) 843-6203

# Atty Docket No. Application No.: MURT P095X1 10/629,092 Information Disclosure Applicant: Statement By Applicant (Use Several Sheets if Necessary) Atty Docket No. Application No.: MURT P095X1 10/629,092 Applicant: Masahiko Kawase Filing Date Group July 28, 2003 3729

## **U.S. Patent Documents**

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class -	Date
	A						_
	В						
	C						
	D						
	Е						
	F						
	G						
	H						
	I						

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Translation	
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	J	9-69418	03/11/97	JP				X
	K	2-189901	07/25/90	JP				X
	L	3-60148	03/15/91	JP		<u> </u>	<u> </u>	X
	M	5-275958	10/22/93	JP				X
	N							

### **Other Documents**

		Other Decaments
Examiner		
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	0	
	P	
	0	
	`	
Examiner		Date Considered
L		

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.